



THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of

Morishita

Serial No.: 09/619,669

Group Art Unit: 2814

Filed: July 19, 2000

Examiner: T. Tran

For: AN INPUT/OUTPUT PROJECTION DEVICE FOR A SEMICONDUCTOR
INTEGRATED CIRCUIT

Honorable Assistant Commissioner of Patents
Washington, D.C. 20231

6/B
D. Scott
11-16-01

AMENDMENT UNDER 37 C.F.R. §1.111

Sir:

In response to the Office Action dated August 29, 2001, please amend the above-identified application as follows.

IN THE CLAIMS:

Please revise claim 1 to read as follows:

B1
sub
C1

1. (Amended) An input/output protection device for a semiconductor integrated circuit having a substrate of a first conduction type, an internal circuit, an input/output terminal, electrode wiring, and signal wiring, said protection device comprising:

a first diffusion layer fabricated in a region of the first conduction type of the semiconductor substrate, the layer having a second conduction type opposite the first conduction type and being connected to the input/output terminal;

a second diffusion layer of the second conduction type being held at a predetermined